

## TRANSMITTAL

Electronic Version v1.1

Stylesheet Version v1.1.0

<b>Title of Invention</b>	METHOD OF MANUFACTURING GATE, THIN FILM TRANSISTOR AND PIXEL	
Application Number :		
Date :		
First Named Applicant:	Mr. Ta-Jung Su	
Confirmation Number:		
Attorney Docket Number:	13129-US-PA	
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Submitted By:		Elec. Sign.
Dr. Belinda Lee Registered Number: 46863		/Belinda Lee/ Sign. Capacity Agent

Documents being submitted:	Files
us-assignment	13129-usassn.xml us-assignment.xsl us-assignment.dtd 13129assignment01.tif 13129assignment02.tif
us-request	13129-usrequ.xml us-request.dtd us-request.xsl
us-fee-sheet	13129-usfees.xml us-fee-sheet.xsl us-fee-sheet.dtd
application-body	13129usf.xml application-body.dtd Image1.tif Image2.tif Image3.tif Image4.tif isoamsa.ent isoamsb.ent isoamsc.ent isoamsn.ent isoamso.ent isoamsr.ent isobox.ent isocyr1.ent isocyr2.ent isodia.ent isogr1.ent isogr2.ent isogr3.ent isogr4.ent isolat1.ent isolat2.ent isomfrk.ent isomopf.ent isomscr.ent isonum.ent isopub.ent isotech.ent mathml2.dtd mathml2-qname-1.mod mmlalias.ent mmlextra.ent soextblx.dtd us-application-body.xsl wipo.ent
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<b>Comments</b>	
<p>Please be kindly advised that the indicated fee of this EFS application is intended to be paid by credit card. However, in the event that the payment through credit card is unsuccessful, the Commissioner is authorized to charge the required fee in connection with this application to Deposit Account No. 50-2620(Order No.13129-US-PA). Deposit Account Name: Jianq Chyun Intellectual Property Office, Authorized User Name: Belinda Lee, E-mail: Belinda@jcipgroup.com.tw; usa@jcipgroup.com.tw</p>	